



Inventor: Jai Subramanian

Title: Methods of Forming Pluralities of Microelectronic Lids

Assignee: Honeywell International Inc.

Docket No. H0001862(4015)-div1

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References – See Attached Form PTO-1449

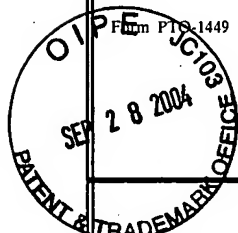
The attached form PTO-1449 is submitted in compliance with 37 CFR § 1.56. Copies of the cited art are included with the exception of U.S. patents and published U.S. applications (1273 Off. Gaz. Pat. Off. 55, 05 August 2003). No admission is made regarding whether all the submitted references are prior art.

The listed U.S. and foreign patents were cited in the International Search Report dated January 20, 2003 received in the related PCT Application, PCT/US01/44849, and are now being submitted in this Supplemental Information Disclosure Statement. However, in the event that a fee is required for filing this Supplemental Information Disclosure Statement, please charge the fee specified under 37 C.F.R. §1.17(p) to Deposit Account No. 23-0925. Please credit Deposit Account No. 23-0925 with any overpayment of the above fee.

Respectfully submitted,

Dated: September 28, 2004

By: Jennifer J. Taylor
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Form PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE				ATTY. DOCKET NO. H0001862(4015)-div1		SERIAL NO. 10/823,117	
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT Jai Subramanian			
				FILING DATE April 12, 2004		GROUP 2831	
U.S. PATENT DOCUMENTS							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	6,075,289	06-2000	Distefano			
	AB	5,482,898	01-1996	Marrs			
	AC	6,068,051	05-2000	Wendt			
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Subclass	Translation
							Yes No
	AM	0 174 020	03-1986	EPO			
	AN	11-195680	07-1999	JP			x
	AO						
	AP						
	AQ						
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)							
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